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Mashiko, K. Maegawa, S. and Inuishi, M. 2001. Feasibility of 0.18 μm SOI CMOS technology using hybrid trench isolation with high resistivity substrate for embedded RF/analog applications. IEEE ...

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Discover a fresh approach to efficient and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide. Expert authors present a sizing methodology that

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COMP_ENG 391: CMOS VLSI Circuits Design

SICK unveiled the first in a new
generation of Ruler3000 3D
streaming cameras. The

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Ruler3000 line combines SICK's
Ranger3 streaming camera with a
Class 2 eye-safe laser, pre-
selected optics and fixed ...

**SICK unveils new generation
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Mobix Labs Inc., a fabless RF (radio frequency) component company focused on next-generation wireless technologies, ...

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External Trade Organization

Work aimed at extending the maximum frequency capability of CMOS technology, described in Chapters 5 and ... which provides a tunable linear resistive element for integrated circuit filter realisation ...

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Chapter 8: Gallium Arsenide Analogue Integrated Circuit Design Techniques

Finishing up on the topic of CMOS bus logic I am going to show a ...
Switching gears I am going to talk a bit about the physical layout of

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integrated circuits. Shown is an
old wafer I have ...

How CMOS Works: Some Final Words About CMOS

A top-down guide to the design of
digital integrated circuits. Reflects
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from VLSI architecture design to CMOS fabrication. Practical hints and tips, case studies, and ...

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In this paper, a novel approach for very stable physical unclonable function (PUF) is presented based on randomly generated via-hole formation using standard CMOS ...
integrated circuits. To guarantee ...

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**VIA PUF: Ultimately Stable
PUF Design using Random Via
Formation in Standard CMOS
Technology**

The Belgian research institute
expects a prototype in standard
28-nm technology to be ready by

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the end of 2018. At the recent International Microwave Symposium in Philadelphia, Imec announced ...

Imec Demonstrates 140-GHz CMOS Radar-on-Chip with Integrated Antennas

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Wireless chipset / SoC design choices are driven by many orthogonal ... is moved to the RF chip and eventually enables an integrated single chip solution. The integration benefits of CMOS The key to ...

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System on Chip (SoC) for Short Range Wireless - CMOS versus SiGe

This structure also lets the company design membranes that are either flat or curved. The microphone chip measures 2.5 mm² and the diaphragm-has a

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1-mm diameter. The company
says its CMOS MEMS ...

Hear this: CMOS microphones on a chip

of six-transistor SRAM-bit cells
with an area less than 0.25
square microns—half the size of

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earlier solutions—using conventional bulk CMOS technology and 45nm design rules. Crolles 2 is the ...

Alliance Produces Ultra-Dense SRAM Cell In 45nm CMOS Technology

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reduce design space and complexity. Omnivision also features advanced High Dynamic Range (HDR), which helps ensure high-quality image production in various lighting conditions for numerous ...

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3 New Image Signal Processors Target Power, Speed, and Autonomous Driving

Dual work function metal gates are integrated at 17nm spacing between n- and pFETs, highlighting the key benefit of

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forksheet devices for advanced
CMOS area scaling ...

Alternatively, with a forksheet ...

Imec demoes integrated forksheet FETs for 2nm processes

May 27, 2021 (Market Insight

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Solution Manual Reports) -- "CMOS High-speed
Cameras Market ... Mikrotron,
Optronis, Integrated Design Tools,
AOS Technologies, Fastec
Imaging, Weisscam, Del Imaging
Systems ...

CMOS High-speed Cameras

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Market Size 2021 by Consumption, Volume, Average Price, Revenue, Market Share and Trend to 2028

One technology used in the industry to mitigate this tradeoff is fully depleted silicon-on-

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insulator (FD-SOI) technology
which can operate at 75% lower
power as compared to a bulk
CMOS process ... “We ...

Lattice Leverages FD-SOI for New Low Power FPGAs

With our third generation

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Keystone DSP design, and the power advantages of 5nm CMOS technology, we are directly addressing our customers' critical needs for low power, highly integrated, high ...

MaxLinear Showcases

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**Industry's First 5nm CMOS
800G PAM4 DSP on TSMC
Advanced Process at OFC
2021**

Intevac, Inc. (Nasdaq: IVAC)
announced today that it has
received two additional Phase 1
development program awards in

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Photonics. These awards are ...

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